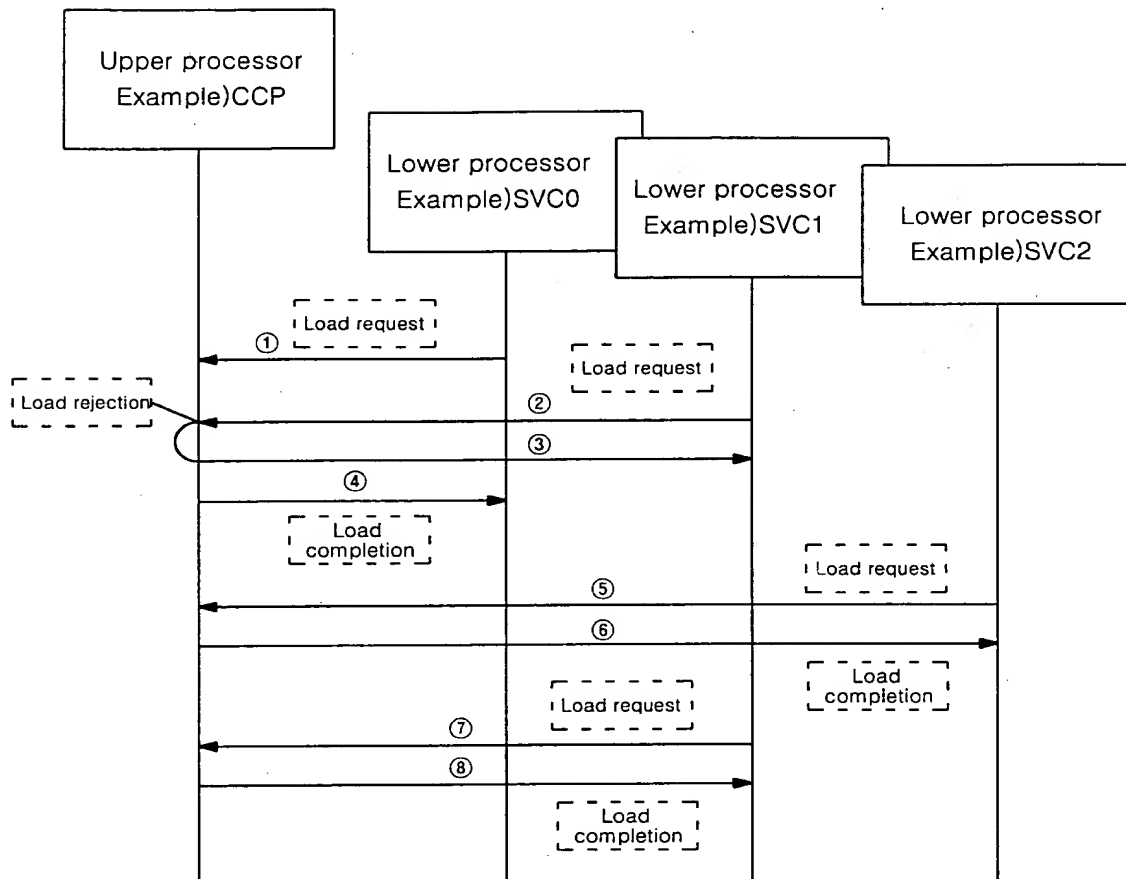


FIG. 1

BACKGROUND ART



DocId: 34567890

FIG. 2
BACKGROUND ART

Information contained in a memory of
a upper processor

```
typedef struct{
    int      zip_add_start;
    short    ld_sts;
    char     unzip_sts;
    int      zip_len;
    int      pack_no;
    short    last_len;
    char     ldfname[12];
    short    ld_sts;
    int      start_addr;
    unsigned chk_sum1;
    unsinged chk_sum2;
    char     version[32];
    char     data[MAX_IPC]
}T_MutiLoad;
```

Pre-defined IPC format

Destination Address	4bytes
Source Address	4bytes
Type	1byte
Control	1byte
Sig ID	2bytes
Length	2bytes
Data	200bytes

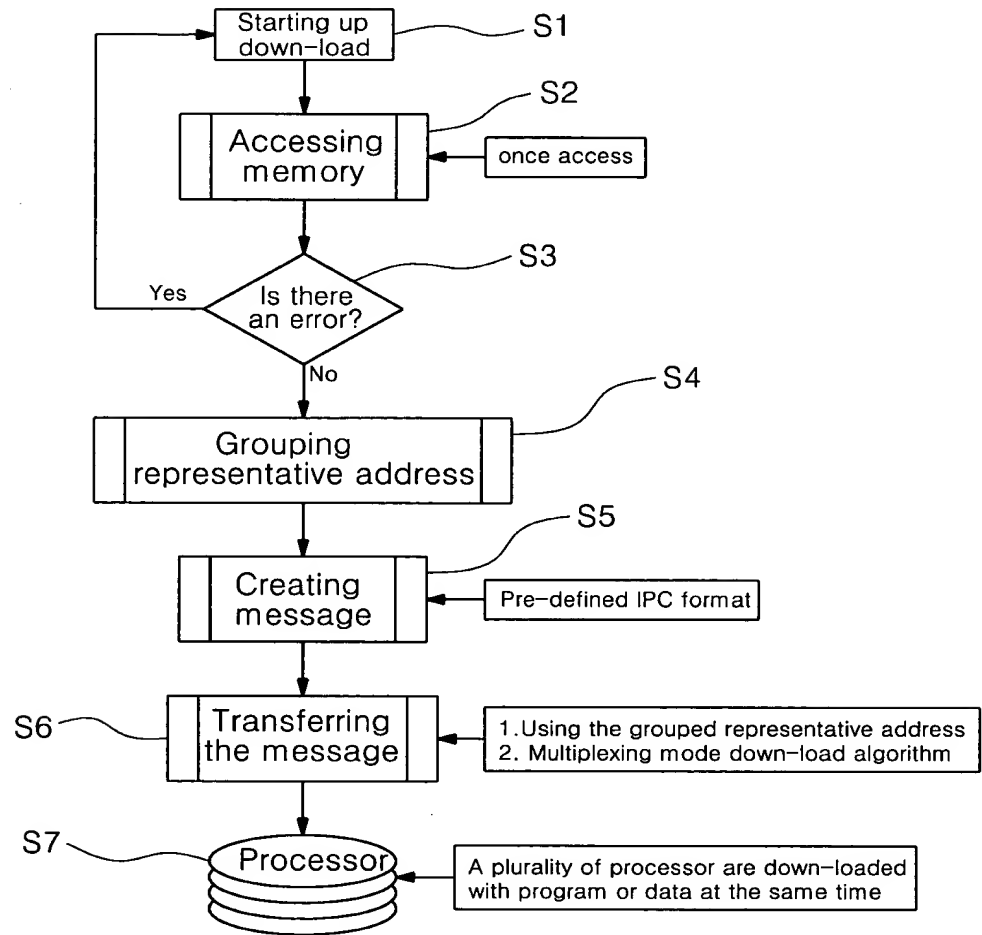
FIG. 3

Address of 4bytes

NA
BA
CA
SA

NA(Node Address) : Processor element ID
BA(BHIU Address) : Processor ID
CA(Cinu Address) : Network group ID 1
SA(Slot Address) : Network group ID 2

FIG. 4



009727" 2E49E460